

The Department of Electronics and Communication Engineering hosted a Core Teaching on Physical Design Verification on 16th February 2024 at AV HALL, Central Library. Dr.M.Devaraju, HOD/ECE welcomed the gathering. Dr.K.Rahimunnisa, Asso.Prof./ECE introduced the speaker, Mr.M.Poovarasam, Project Associate at RISE Lab, IIT Madras. He delivered a succinct yet informative lecture on physical design and verification covering essential topics such as the design flow, physical design process, verification techniques, and advanced topics like design closure and DFT, the lecture provided attendees with valuable insights into semiconductor manufacturing. Through case studies and industry insights, Mr.M.Poovarasam effectively conveyed the complexities and best practices in IC design, inspiring participants to delve deeper into the field. Dr.D.Jessintha Asso.Prof./ECE gave the Vote of Thanks. Overall, the lecture served as a platform for knowledge exchange and collaboration, enriching attendees with practical knowledge essential for success in semiconductor design and verification.

Convener:Dr.M.Devaraju,HOD/ECE

Coordinators :

Dr.K.Rahimunnisa,ASP/ECE

Dr.D.Jessintha,ASP/ECE





GPS Map Camera



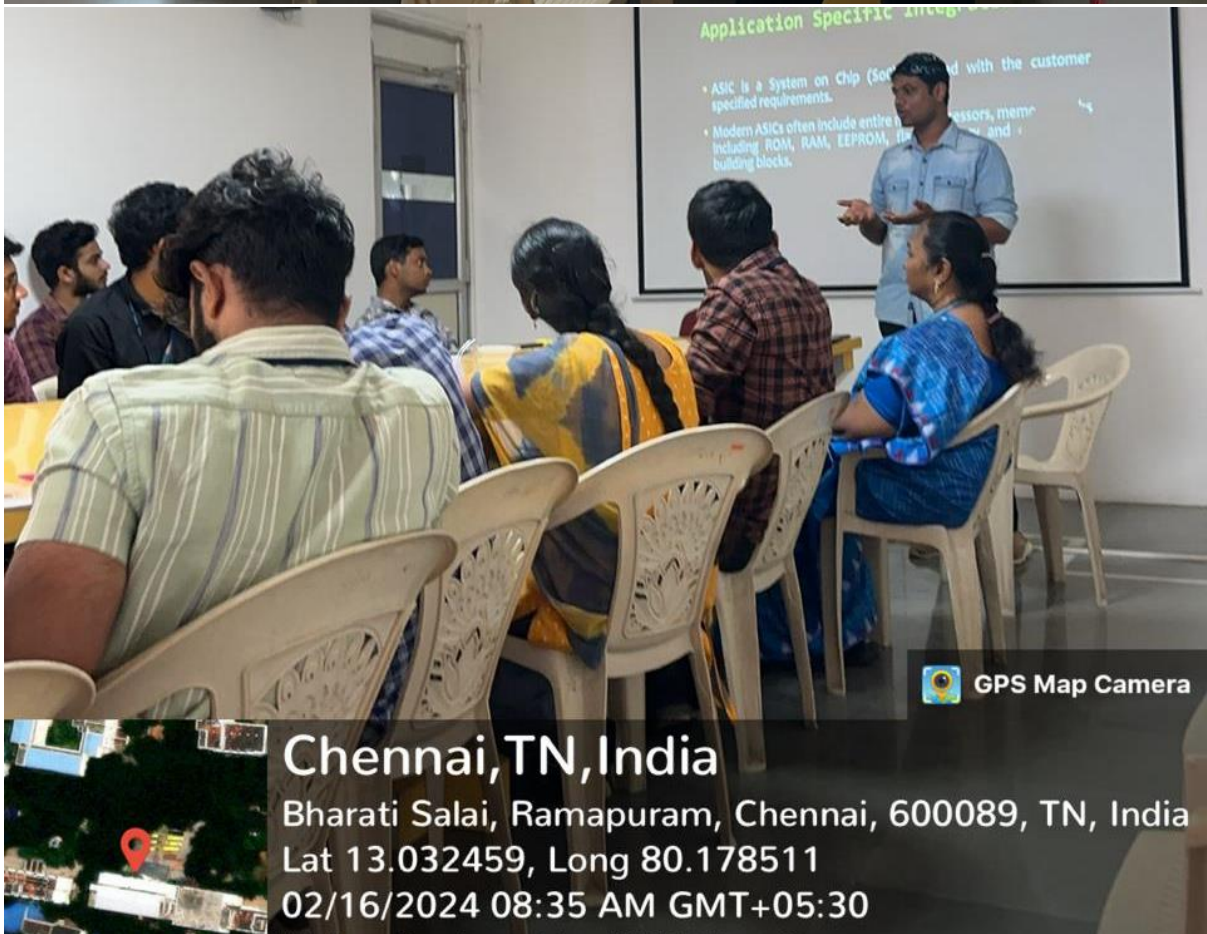
**Chennai, TN, India**

Bharati Salai, Ramapuram, Chennai, 600089, TN, India

Lat 13.032459, Long 80.178496

02/16/2024 08:33 AM GMT+05:30

Note : Captured by GPS Map Camera



GPS Map Camera



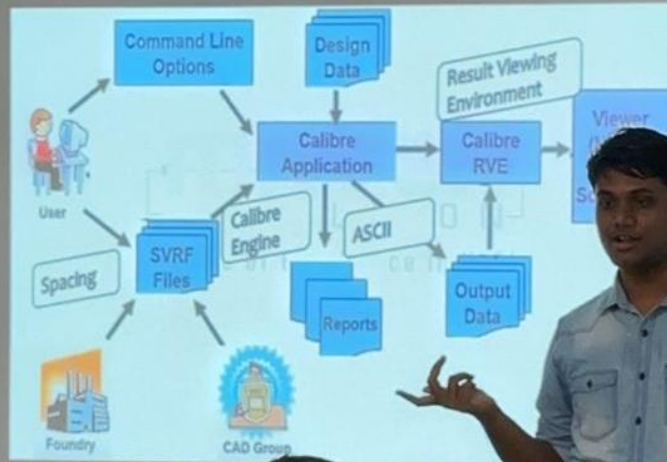
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Lat 13.032459, Long 80.178511

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## Calibre Tool Flow



GPS Map Camera

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Lat 13.032475, Long 80.178503

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## Application Specific Integrated Circuit

- ASIC is a System on Chip (SoC) designed to meet the customer specified requirements.
- Modern ASICs often include entire microprocessor memory blocks including ROM, RAM, EEPROM, flash memory and other large building blocks.

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